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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/020,968
		Filing Date	12/19/2001
		First Named Inventor	Steven G. Schmidt
		Art Unit	2616
		Examiner Name	Habte Mered
Sheet 1	of 1	Attorney Docket Number	2914

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS); title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
HM		GREGORY L. FRAZIER & YUVAL TAMIR, The Design and Implementation of a Multi-Queue Buffer for VLSI Communication Switches, 1989 IEEE	
HM		KOHEI SHIOMOTO, MASAYUKI MURATA, YUJI OIE & MIDEO MIYAHARA, Performance Evaluation of Cell Bypass Queuing Discipline for Buffered Banyan Type ATM Switches, 1990 IEEE	

Examiner Signature	/Habte Mered/	Date Considered	12/08/2006
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/Habte Mered/



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Sheet	1	of	1
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[illegible][illegible]

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LIST OF ART CITED BY APPLICANT (PTO-1449)				ATTY. DOCKET NO. 87264.2560		SERIAL NO. 10/020,968	
				APPLICANT SCHMIDT			
				FILING DATE 12/19/01		GROUP 2661 2662	
U.S. PATENT DOCUMENTS							
	EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A	MM	6,078,959	06/20/00	Wright et al.	709	227	01/29/98
B							
C							
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FOREIGN PATENT DOCUMENTS							
	EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
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